GEETHANJALI COLLEGE OF ENGINEERING AND TECHNOLOGY
(UGC AUTONOMOUS)
Cheeryal (V), Keesara (M), Medchal Dist – 501 301, Telangana

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Action Taken Report on the observations made by the Committee during the External Academic and Administrative Audit carried out for the A.Y.2024-25

The External Academic and Administrative Audit for the A.Y:2024-25 was conducted on 6ᵗʰ December 2025, with Dr. T. Madhavi Kumari, Associate Professor, Department of ECE, JNTUH as the external member of the committee.

A comprehensive report outlining the strengths, weaknesses, concerns, and recommendations, derived from the observations made during the audit, is submitted to the institute. The report given by the external committee was discussed with the senior faculty of the department and corrective actions were initiated as detailed below:

Remarks category	Remarks made by the committee	Action Taken
Strengths	• Lab facilities are of state of the art. • More teachers are long standing means college is maintaining remedies to retain the faculty • Checking the development status of the college is a good practice.	---------
Weaknesses	PG course should be introduced.	PG courses (M.Tech.) with specialization in Embedded Systems, VLSI Design and Electronics & Communication Engineering were already introduced long back. As the number of students joining these PG courses was on the decline year after year, the courses could not be continued subsequently. However once these courses regain their demand, they will be re-introduced.

	More real-time projects should be taken and see them to convert into products.	• Department has been encouraging the students to take up real-time projects as their Mini Projects/ Projects. • Separate amount has been earmarked in the budget to encourage the students to take up these projects/ Projects under PBL. • Some of the projects are completely funded by the college to encouraging the students. • About 50% of the Mini Projects/ Projects which are product based in nature are exhibited as part of "NAVARITH PRADARSHAN", an annual project exhibition conducted every year. • Thirteen projects of the AY2024-25 were published in SCOPUS indexed journals which reflect the quality of Mini Projects/ Projects. • However more efforts will be made to see that students take up more real-time projects and convert them into useful products.
	More financial help to take up research projects.	College has been assisting financially to encourage both faculty and students to take up research projects by clearly allocating the following amounts in the yearly Budget: **For faculty** Seed money for R&D – Rs. 4.00 lakhs **For Students** Students' Projects – Rs. 2,50,000/- Projects under PBL – Rs.2,00,000/- Working Models – Rs. 2,00,000/- A couple of faculty members, namely, Dr. P. Sudhakar and Dr. M. Jagan Mohana Rao were granted a seed amount of Rs. 2.00 lakh each for the AY 2024-25, based on the research proposals they have made. Some of the student projects have also been funded completely by the college.

Concerns	Moderation should be accepted.	Moderation of the Question Papers for the Semester End Examinations is being carried out strictly in line with the stipulations/ guidelines provided by JNTUH, which doesn't allow any modifications in the question paper even when the questions are given from outside the syllabus or if some data is missing. A decision regarding moderation needs to be taken at the college level after taking approval from JNTUH.
	Placement for Students	Student Placements have been reduced from 172 during the AY 2023-24 to 122 for the AY 2024-25. Following are some of the reasons for reduced placements: • Top IT services companies (like TCS, Infosys, Wipro, HCL) have reduced campus hiring. • Hiring by IT services (which once recruited in big numbers) has shrunk continuously as companies are gradually becoming automated. • Work that previously required large teams of freshers can now be done with smaller, more skilled teams or automation tools. This affects companies' need to hire hundreds of freshers from campuses. However every effort will be made by the department to improve the placements. Towards this, the department is offering special training in core areas such as VLSI and Embedded Systems involving industry people.
Recommendations	To go for PG course	This is already discussed above under the heading "Weaknesses of the department"

	To have core industry institution tie-up.	• The department has MoU with 15 core companies • These industries facilitate internships, industrial visits, training programs in core areas and projects for students. • The department has 5 industry supported laboratories • FDPs and Student workshops are conducted with resource persons from industries. The department will certainly work towards further enhancing the tie-ups with core industries.
Suggestions for improvement	More Ph.Ds in teaching faculty	About 30% of the existing faculty have Ph.D. Another 14 faculty members are pursuing their Ph.D.
	Product development for everyday life problems	• Towards this, the department has been encouraging the students to take up societal problems as their Projects/ Mini Projects to provide engineering solutions. • Students, as part of the course "Design Thinking", identify the societal problems and provide engineering solutions. • Many of these products developed for solving day-to-day problems of the society are on display in the Projects Lab of the department.

Brief report if any:
- The department is doing extremely good with a goal to enhance the student technical skills with the state of the art technologies and in emerging areas to make them very talented electronic engineers.
- Department is maintaining the information very meticulously on toes as and when it is asked.

Dean, SE&CE   HoD