

## Impact of Mentoring of Group Head on Faculty Mentees

**Name of Group: Fields and Waves**

**Academic Year: 2019-20**

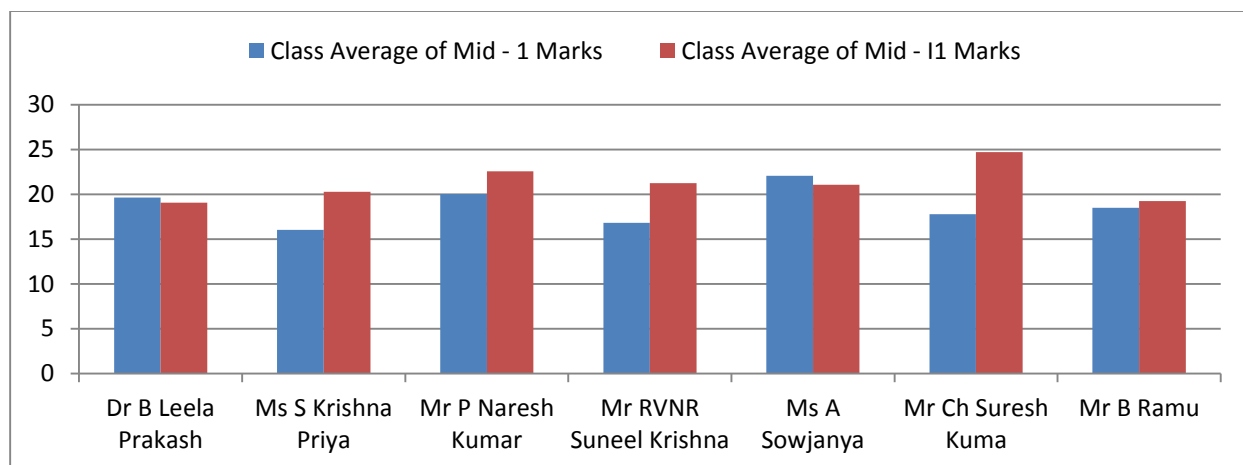
**Semester: I**

**Group Head: Dr RS Raju**

| S.N. | Name of the Faculty Mentee | Course taught | Class Average of Mid - 1 Marks | Class Average of Mid - I1 Marks |
|------|----------------------------|---------------|--------------------------------|---------------------------------|
| 1    | Dr B Leela Prakash         | AWP           | 19.64                          | 19.08                           |
| 2    | Ms S Krishna Priya         | MWE           | 16.05                          | 20.31                           |
| 3    | Mr P Naresh Kumar          | AWP           | 20.06                          | 22.57                           |
| 4    | Mr RVNR Suneel Krishna     | MWE           | 16.82                          | 21.26                           |
| 5    | Ms A Sowjanya              | AWP           | 22.07                          | 21.08                           |
| 6    | Mr Ch Suresh Kuma          | MWE           | 17.78                          | 24.74                           |
| 7    | Mr B Ramu                  | CMC           | 18.52                          | 19.26                           |

Any other specific improvement observed in a mentee faculty:

| S.No | Name of the Faculty Mentee | Improvement observed based on the suggestion(s)  |
|------|----------------------------|--|
| 1    | Dr B Leela Prakash         | Projects were done under project-based learning activity.  |
| 2    | Ms S Krishna Priya         | She published a paper in Scopus indexed journal in microwave engineering.<br>Projects were done under project-based learning activity.         |
| 3    | Mr P Naresh Kumar          | Projects were done by students with a close supervision under project-based learning activity.<br>Participated in NPTL to strengthen knowledge |
| 4    | Mr RVNR Suneel Krishna     | Projects were done by students with a close supervision under project-based learning activity.<br>Participated in NPTL to strengthen knowledge |
| 5    | Ms A Sowjanya              | Projects were done by students with a close supervision under project-based learning activity.   |
| 6.   | Mr Ch Suresh Kumar         | Projects were done by students with a close supervision under project-based learning activity.<br>Participated in NPTL to strengthen knowledge |
| 7    | Mr B Ramu                  | A paper was published in scopus indexed journal in the field of ordino.<br>Participated in NPTL to strengthen knowledge                        |



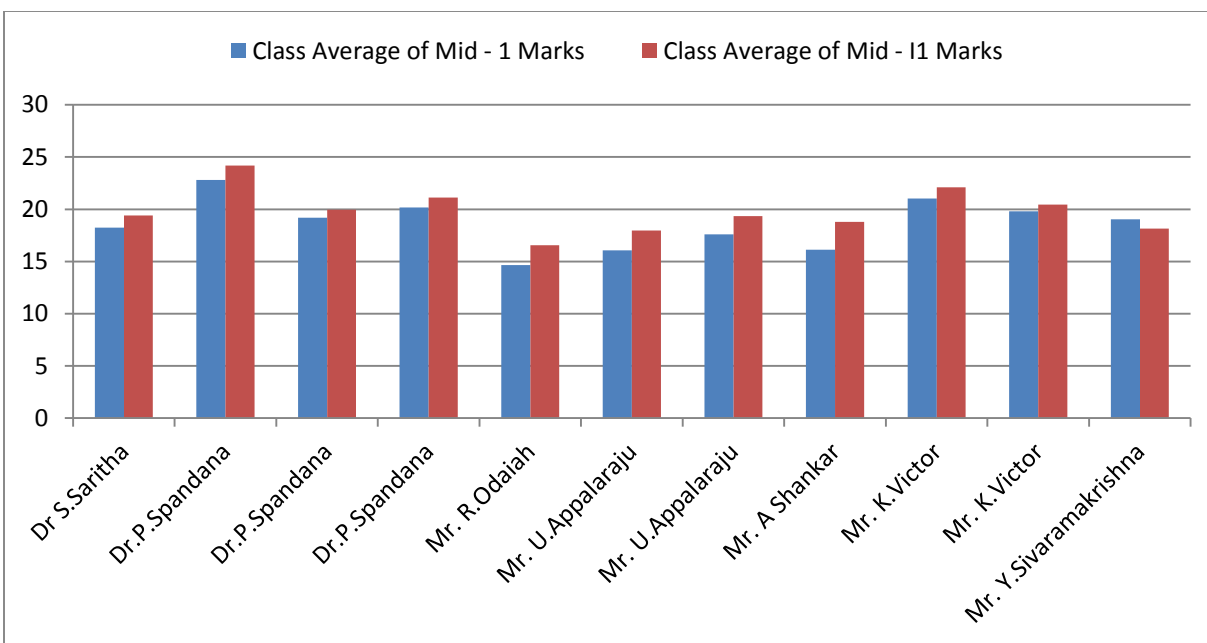
**Group - II – Signal processing and control systems**

**Semester: I**

**Name of Group Head: Dr C V Narasimhulu**

**Academic Year: 2019-20**

| S.N. | Name of the Faculty Mentee | Course taught                                       | Class Average of Mid - 1 Marks | Class Average of Mid - I1 Marks              | Remarks if any  |
|------|----------------------------|---|--------------------------------|--|---|
| 1    | Dr S.Saritha               | MPMC ,III ECE D                                     | <b>18.23</b>                   | <b>19.41</b>                                 | Improvement   |
| 2    | Dr.P.Spandana              | DIP, IV, ECE,A<br>DIP, IV, ECE,B<br>DIP, IV, ECE ,D | 22.81<br>19.19<br>20.18        | <b>24.18</b><br><b>19.95</b><br><b>21.12</b> | Improvement   |
| 3    | Mr. R.Odaiah               | SS, II, ECE,D                                       | <b>14.66</b>                   | <b>16.56</b>                                 | Improvement   |
| 4    | Mr. U.Appalaraju           | ECAD, II, ECE, B<br>ECAD, II, ECE, C                | <b>16.05</b><br><b>17.60</b>   | <b>17.96</b><br><b>19.33</b>                 | Improvement   |
| 5    | Mr. A Shankar              | DSP, II, EEE, A                                     | <b>16.14</b>                   | <b>18.8</b>                                  | Improvement   |
| 6    | Mr. K.Victor               | CMC, IV, ECE,A<br>CMC, IV, ECE,D                    | <b>21.03</b><br><b>19.8</b>    | <b>22.09</b><br><b>20.44</b>                 | Improvement   |
| 7    | Mr. Y.Sivaramakrishna      | LDICA, III, ECE, B                                  | <b>19.05</b>                   | <b>18.15</b>                                 | Needs Improvement, however 18.15 is a reasonable average mark |



Any improvement observed in a mentee faculty:

| S.N. | Name of the Faculty Mentee | Improvement observed based on the suggestion(s)   |
|------|----------------------------|---|
| 1    | Dr S.Saritha               | <ul style="list-style-type: none"> <li>Attended AICTE Sponsored FDP on “Advances in Signal and Image Processing with hands-on training using MATLAB” during 18-30, November 2019</li> <li>Interacted time-to-time on how to improve teaching-learning process right from the beginning of semester.</li> <li>Weekly mentoring of students and contacting parents on performance</li> </ul>  |
| 2    | Dr.Spandana                | <ul style="list-style-type: none"> <li>Attended AICTE Sponsored FDP on “Advances in Signal and Image Processing with hands-on training using MATLAB” during 18-30, November 2019</li> <li>Close interactions were held on a weekly basis.</li> <li>Weekly mentoring of students and contacting parents on performance</li> </ul>  |
| 3    | Mr. R.Odaiah               | <ul style="list-style-type: none"> <li>Attended AICTE Sponsored FDP on “Advances in Signal and Image Processing with hands-on training using MATLAB” during 18-30, November 2019</li> <li>Suggestions were given to conduct more tutorial problems, assignments and class tests.</li> <li>Emphasized on viewing NPTEL lectures</li> <li>Weekly mentoring of students and contacting parents on performance</li> </ul>   |
| 4    | Mr. U.Appalaraju           | <ul style="list-style-type: none"> <li>Participated as a Delegate at Seventh International Conference on TRANSFORMATIONS IN ENGINEERING EDUCATION (ICTIEE-2020) held during 5th to 8th January 2020, organized by Anurag Group of Institutions, Hyderabad</li> <li>Attended AICTE Sponsored FDP on “Advances in Signal and Image Processing with hands-on training using MATLAB” during 18-30, November 2019</li> <li>Co-ordinator for Two Day Workshop on “ Data Science using python” Organized by Department of ECE, Geethanjali College of Engineering and Technology, Hyderabad with Swecha organization during 26nd – 27th September, 2019</li> </ul> |

|   |                       |   |
|---|-----------------------|---|
|   |                       | <ul style="list-style-type: none"> <li>• Suggestions were given to conduct more tutorial problems, assignments and class tests.</li> <li>• Emphasized on viewing NPTEL lectures</li> <li>• Weekly mentoring of students and contacting parents on performance</li> </ul>  |
| 5 | Mr. A Shankar         | <ul style="list-style-type: none"> <li>• Suggestions were given to conduct more tutorial problems, assignments and class tests.</li> <li>• Emphasized on viewing NPTEL lectures</li> <li>• Weekly mentoring of students and contacting parents on performance</li> </ul>  |
| 6 | Mr. K.Victor          | <ul style="list-style-type: none"> <li>• Attended AICTE Sponsored FDP on “Advances in Signal and Image Processing with hands-on training using MATLAB” during 18-30, November 2019</li> <li>• Suggestions were given to conduct more tutorial problems, assignments and class tests.</li> <li>• Emphasized on viewing NPTEL lectures</li> <li>• Weekly mentoring of students and contacting parents on performance</li> </ul> |
| 7 | Mr. Y.Sivaramakrishna | <ul style="list-style-type: none"> <li>• Attended AICTE Sponsored FDP on “Advances in Signal and Image Processing with hands-on training using MATLAB” during 18-30, November 2019</li> <li>• Suggestions were given to conduct more tutorial problems, assignments and class tests.</li> <li>• Emphasized on viewing NPTEL lectures</li> <li>• Weekly mentoring of students and contacting parents on performance</li> </ul> |

**Dr C V Narasimhulu,**  
Head, Signal Processing group

**Group - III – Communications**

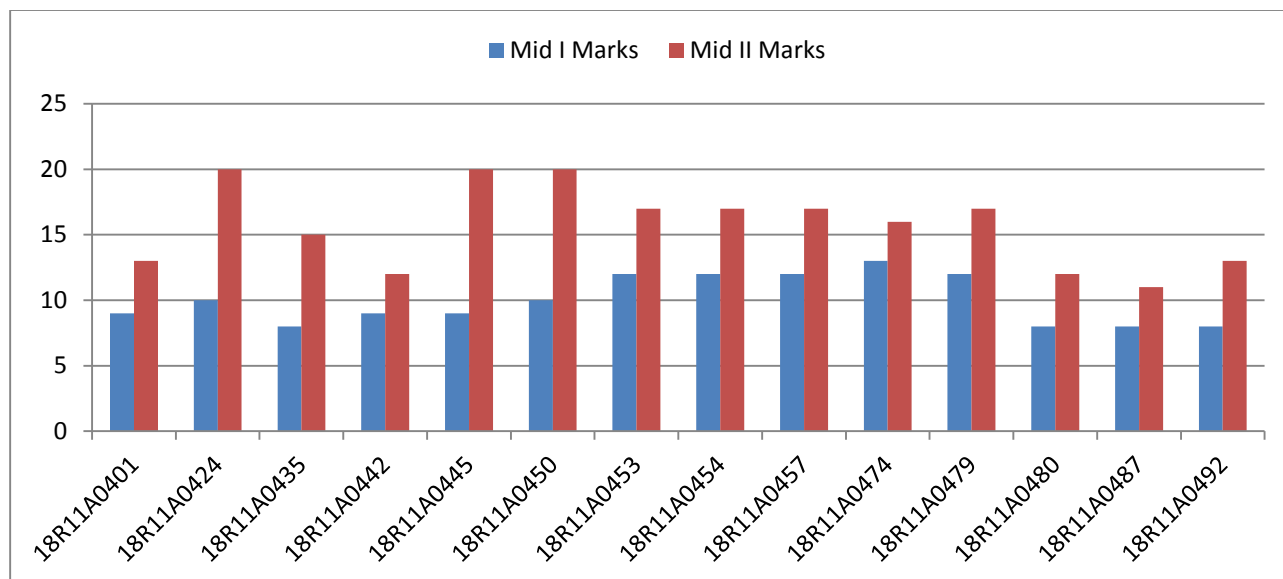
**Name of Group Head: Dr.P.Vijai Bhaskar**

**Academic Year:2019-2020**

**Semester:I**

| S.No | Name of the Faculty Mentee | Course taught              | Class Average of Mid - 1 Marks | Class Average of Mid - II Marks | Remarks if any |
|------|----------------------------|----------------------------|--------------------------------|---------------------------------|----------------|
| 1)   | Mr. D.Venkata Rami Reddy   | Signals and Systems (II-E) | 13.55                          | 15.22                           |                |
| 2)   | Mr. D.Venkata Rami Reddy   | Signals and Systems (II-E) | 24                             | 24                              |                |
| 3)   | Mr. D.Venkata Rami Reddy   | MPMC Lab (III ECE-B)       | 23                             | 23                              |                |
| 4)   | Dr. V. Satya Srinivas      | CMC (IV-C)                 | 16.75                          | 20.40                           |                |
| 5)   | Dr. V. Satya Srinivas      | Satcom (IV-A/B)            | 17.09                          | 24.90                           |                |
| 6)   | Dr. V. Satya Srinivas      | MWE&DC lab (IV-A)          | 13.05/15                       | 13.47/15                        |                |
| 7)   | Ms. M.Sowjanya             | CT (A-Section)             | 17.9                           | 17                              |                |
| 8)   | Ms. B.Suneetha             | Embedded Systems (IV-A)    | 18.81                          | 20.88                           |                |
| 9)   | Ms. B.Suneetha             | Embedded Systems Lab       | 9.05/15                        | 11.72/15                        |                |

|     |                    |               |         |        |  |
|-----|--------------------|---------------|---------|--------|--|
| 10) | Ms. V. Sirisha     | Satcom (IV-E) | 18.52   | 19.26  |  |
| 11) | Ms. V S PadmaPriya | CMC (IV-E)    | 19.66   | 19.5   |  |
| 12) | V S PadmaPriya     | ECAD lab      | 10.6/15 | 9.5/15 |  |
| 13) | Ms. Jyothirmaye    | SS Theory     | 13      | 22     |  |
| 14) | Ms. Jyothirmaye    | SS Lab        | 10/15   | 07/15  |  |



Dr. P. Vijai Bhaskar

**Group – 1V – Digital Electronics**

**Name of Group Head: Prof. K S Rao**

**Academic Year: 2019-20**

**Semester: I**

**a. Effect on Mid-marks :**

| S.N. | Name of the Faculty Mentee  | Course taught   | Class Average of Mid - 1 Marks                           | Class Average of Mid - II Marks   | Remarks if any                 |
|------|---|---|--|---|--------------------------------|
| 1    | Prof. K S Rao   | DD – ECE 2-1A & B   | 15.1   | 17  | Improvement                    |
| 2    | Ms. G Sreelakshmi   | MPMC – ECE31A<br>MPMC – ECE31B<br>MPMC Lab – ECE31A                                       | 19.09<br>19.56<br>11.87/15                               | 19.79<br>19.84<br>12.83/15  | Improvement                    |
| 3    | Ms B Sreelatha<br>With Suresh kumar<br>With A Sowjanya<br>With Suresh kumar | DD, ECE 2C<br>DD, EEE 2B<br>DD LAB, ECE 2C<br>DD LAB, ECE 2E<br>DD LAB, ECE 2D (BATCH-D2) | 19.34<br>17.16<br>11.97 / 15<br>10.57 / 15<br>11.02 / 15 | 19.40<br><b>14.95</b><br><b>10.24 / 15</b><br><b>10.46 / 15</b><br>11.45 / 15 | Improvement<br><br>Improvement |
| 4    | Ms. M Laxmi   | MPMC-ECE-31E<br>ES-ECE41B   | 18.48<br>15.70   | 19.75<br>17.69  | Improvement                    |

|   |                                 |  |   |  |             |
|---|---------------------------------|--|---|--|-------------|
|   | With M Anand<br>With B Suneetha | MPMC Lab-ECE-31E<br>ES Lab – ECE41B  | 9.95 /15<br>9.27  | 10.48/15<br>11.13  |             |
| 5 | Mr. Ch Sandeep                  | ES –ECE41C<br>ES- ECE 41E<br>MPMC-EEE-31A<br>ESLab-ECE41C<br>ESLab-ECE 41E<br>MPMCLab-EEE31A | 15.83<br>13.54<br>14.01<br>11.17/15<br>13.04/15<br>9.9/15 | 16.64<br>15.91<br>16.17<br>11.58/15<br>12.79/15<br>10.6/15 | Improvement |
| 6 | Ms M Uma Rani                   | DD – ECE-21D   | 15.75   | 17.13  | Improvement |
| 7 | Mr. M Anand                     | ES - ECE-41D   | 12.3  | 15.89  | Improvement |

b. Any other specific improvement observed in a mentee faculty:

| S.N. | Name of the Faculty Mentee | Improvement observed based on the suggestion(s)  | Remarks if any  |
|------|----------------------------|--|---|
| 1    | Ms. G Sreelakshmi          | <ul style="list-style-type: none"> <li>published paper on Vinculum BCD Compressors</li> <li>NPTEL courses Micro electronics to Devices and Microprocessors and controllers</li> <li>Applied for Project proposal on Air pollution</li> </ul>   | Actively pursuing PhD course  |
| 2    | Ms B Sreelatha             | <ul style="list-style-type: none"> <li>Frequent interactions were held on how to improve teaching-learning process right from the beginning of semester.</li> <li>Suggestions were given to conduct more tutorial problems, assignments and class tests.</li> <li>Emphasized on viewing NPTEL lectures.</li> </ul> <p>Motivating the students to execute the project under project based learning.</p> | Actively involved in the PBL training in DD course.                             |
| 3    | Ms. M Laxmi                | <ul style="list-style-type: none"> <li>Frequent interactions were held on how to improve teaching-learning process right from the beginning of semester.</li> <li>Suggestions were given to conduct more tutorial problems, assignments and class tests.</li> <li>Emphasized on viewing NPTEL lectures</li> </ul>  |   |
| 4    | Mr. Ch Sandeep             | <ul style="list-style-type: none"> <li>attended workshop RRSPBL2019 to get acquaintance regarding Project based learning.</li> </ul>   | Taking active participation in PBL.   |
| 5    | Ms M Uma Rani              | <ul style="list-style-type: none"> <li>Frequent interactions were held on how to improve teaching-learning process right from the beginning of the semester.</li> <li>Suggestions were given to conduct more tutorial problems, assignments and class tests.</li> <li>Suggestions were given to motivate the students in doing the projects under project based learning.</li> </ul>                   | Some students have completed their project and participated in the competition. |
| 6.   | Mr. M Anand                | <ul style="list-style-type: none"> <li>Published paper “Border guard spy Robot” in TEST Engineering and Management.</li> <li>Emphasized on attending NPTEL lectures – applied for C language programming.</li> </ul>   | SCOPUS Indexed paper  |

c. Improvement in the TLP:

1. Ms. M Laxmi:

| Sno | Name of the Subject                      | Name of the Faculty | Feedback Term – 1 | Feedback Term – 2 |
|-----|--|---------------------|-------------------|-------------------|
| 1   | Microprocessors and Microcontrollers     | M.Laxmi             | 79.65             | 81.85             |
| 2   | Microprocessors and Microcontrollers Lab | M.Laxmi, M.Anand    | 77.87             | 79.26             |
| 3   | Embedded Systems                         | M.Laxmi             | 80.98             | 86.11             |
| 4   | Embedded Systems Lab                     | M.Laxmi, B.Suneetha | 82.5              | 87.27             |

| S.No. | Subject | Branch | Class | TLP Term I | TLP Term II |
|-------|---------|--------|-------|------------|-------------|
|-------|---------|--------|-------|------------|-------------|

2. Ms. G Sreelakshmi

| Sno | Name of the Subject                  | Section  | Feedback Term – 1 | Feedback Term – 2 |
|-----|--------------------------------------|----------|-------------------|-------------------|
| 1   | Microprocessors and Microcontrollers | III_Asec | 68.68             | 74.22             |
| 2   | MPMC lab                             | III_A    | 76.34             | 75.92             |
| 3   | MPMC                                 | III_B    | 90.78             | 87.25             |
| 4   | Average:                             |          | 78.6              | 69.13             |

3. Ms. B Sreelatha

| Sno | Name of the Subject       | Name of the Faculty          | Feedback Term – 1 | Feedback Term – 2 |
|-----|---------------------------|------------------------------|-------------------|-------------------|
| 1   | DD, ECE 2C                | B SREELATHA                  | 85.89             | 90.94             |
| 2   | DD, EEE 2B                | B SREELATHA                  | 77.15             | 69                |
| 3   | DD LAB, ECE 2C            | B SREELATHA, CH SURESH KUMAR | 86.90             | 89.55             |
| 4   | DD LAB, ECE 2E            | B SREELATHA, A SOWJANYA      | 81.77             | 80                |
| 5   | DD LAB, ECE 2D (BATCH-D2) | B SREELATHA, CH SURESH KUMAR | 88.53             | 79.43             |

4. Ms. M Uma Rani

| Sno | Name of the Subject | Name of the Faculty       | Feedback Term – 1 | Feedback Term – 2 |
|-----|---------------------|---------------------------|-------------------|-------------------|
| 1   | Digital Design      | M.UmaRani                 | 80.4              | 83.07             |
| 2   | Digital Design Lab  | M.UmaRani,Dr.S.Saritha    | 81.61             | 84.69             |
| 3   | Digital Design Lab  | M.UmaRani,Ch.Suresh kumar | 78.33             | 81.66             |

5. Mr. Ch Sandeep

|   |          |     |       |       |       |
|---|----------|-----|-------|-------|-------|
| 1 | ES       | ECE | IV-C  | 79.84 | 79.7  |
| 2 | ES lab   | ECE | IV-C  | 81.3  | 79.44 |
| 3 | ES       | ECE | IV-E  | 87.03 | 86.63 |
| 4 | ES Lab   | ECE | IV-E  | 90.96 | 85.36 |
| 5 | MPMC     | EEE | III-A | 74.37 | 74.33 |
| 6 | MPMC Lab | EEE | III-A | 71.68 | 74.93 |

6. Mr. M Anand

| Sno | Name of the Subject                      | Name of the Faculty   | Feedback Term – 1 | Feedback Term – 2 |
|-----|--|-----------------------|-------------------|-------------------|
| 1   | Embedded Systems                         | M. Anand              | 73.26             | 85.52             |
| 2   | Embedded Systems Lab                     | M. Anand              | 73.24             | 86.50             |
|     | Embedded Systems Lab                     | M. Anand, Ch. Sandeep | 90.96             | 85.36             |
| 3   | Microprocessors and Microcontrollers Lab | M. Anand, Dr. Saritha | 73.86             | 74.50             |
| 4   | Microprocessors and Microcontrollers Lab | M. Anand, M. Laxmi    | 77.87             | 79.26             |

**Group - V - Analog Electronics and Instrumentation**

Name of Group Head: **Dr. S Suryanarayana**

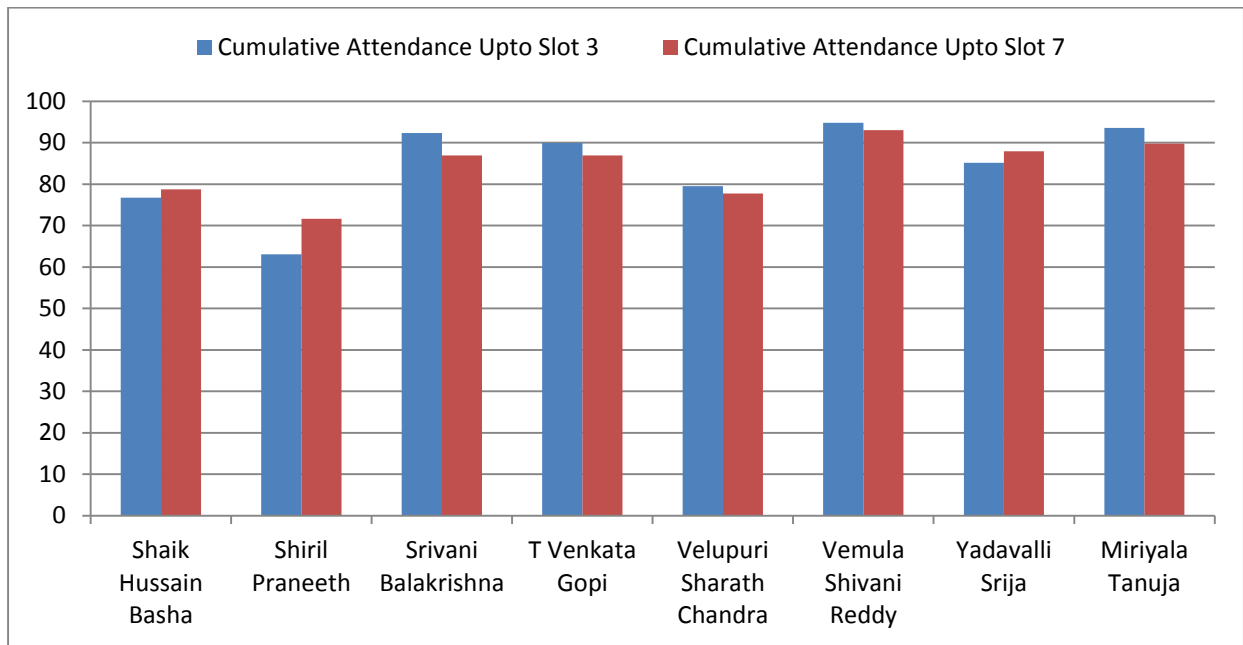
Academic Year: **2019-20**

Semester: **I**

| S.No | Name of the Faculty Mentee | Class Average of Mid - I Marks | Class Average of Mid - II Marks | Remarks, if any  |
|------|----------------------------|--------------------------------|---------------------------------|------------------|
| 1    | Mrs. Y. Naga Laxmi         | 17.5                           | 23                              | Good improvement |



|   |                             |       |       |                  |
|---|-----------------------------|-------|-------|------------------|
| 2 | Mrs. J Mrudula              | 15    | 19    | Good improvement |
| 3 | Mrs. J Mrudula              | 15    | 21    | Good improvement |
| 4 | Mr. A Subrahmanyam          | 17.26 | 19.5  | Good improvement |
| 5 | Mr. A Subrahmanyam          | 17.13 | 20.41 | Good improvement |
| 6 | Mr. M Krishna               | 18    | 21    | Good improvement |
| 7 | Mr. P Chandra Prakash Reddy | 13.68 | 18.0  | Good improvement |
| 8 | Mr. P Chandra Prakash Reddy | 13.29 | 18.9  | Good improvement |

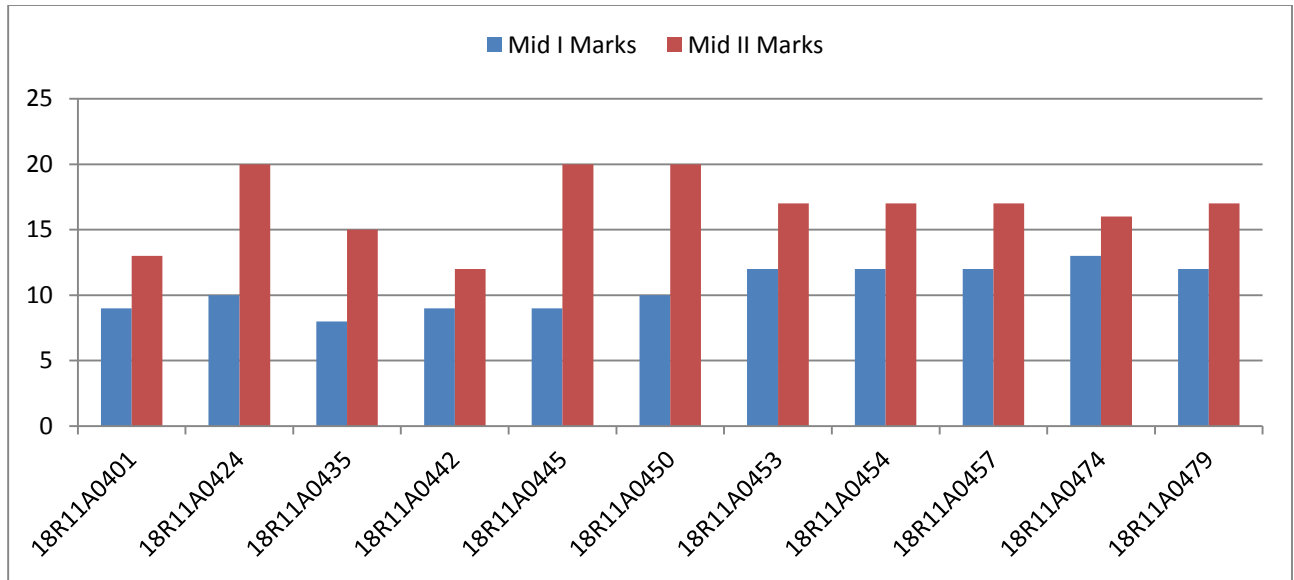


**Group - VI - VLSI****Name of Group Head: O.V.P.R. Siva Kumar****Academic Year: 2019-20****Semester: I**

| S.No | Name of the Faculty Mentee | Course taught          | Class Average of Mid - I Marks | Class Average of Mid - II Marks | Remarks if any      |
|------|----------------------------|------------------------|--------------------------------|---------------------------------|---------------------|
| 1    | Mr. P. Sudhakar            | Circuit Theory         | 15.2/30                        | 18.8/30                         | Improved            |
|      |                            | LDICA                  | 18/30                          | 18.2/30                         | Marginally improved |
|      |                            | ICA & HDL Lab          | 11.9/15                        | 11.6/15                         |                     |
|      |                            | DD Lab                 | 9.9/15                         | 10.9/15                         | Improved            |
| 2    | Mr. M.Krishna Chaitanya    | LDICA                  | 19.9/30                        | 20/30                           | Marginally improved |
|      |                            | ICA & HDL Lab          | 11.9/15                        | 11.6/15                         |                     |
| 3    | Ms. S. Yagna Sree          | MPMC(ECE)              | 16.2/30                        | 17.7/30                         | Improved            |
|      |                            | MPMC Lab C             | 9.7/15                         | 10.7/15                         | Improved            |
| 5    | B.Mamatha                  | MPMC(EEE)              | 14.7/30                        | 16.36/30                        | Improved            |
|      |                            | MPMC lab(EEE)          | 07.88/30                       | 09.68/30                        | Improved            |
|      |                            | ICA&HDL Simulation Lab | 10.48/15                       | 11.51/15                        | Improved            |

Any other specific improvement observed in a mentee faculty:

| S.No | Name of the Faculty Mentee | Improvement observed based on the suggestion(s)  | Remarks if any  |
|------|----------------------------|--|---|
| 1    | Ms.B.Mamatha               | To support advanced learners, motivated her to execute additional project on “Electronic Piano”. Encouraged her to become a member of Centre of Excellence (CoE) in VLSI design. | Procured the components and successfully executed the project with students’ participation. She is groomed as an active member of CoE in VLSI design. |
| 2    | Mr. M. Krishna Chaitanya   | Clarified the concepts of 7-seg drivers/receivers and waveform generators.   | This helped in clarifying the doubts of the students  |
| 3    | Dr.P. Sudhakar             | Encouraged to take up new courses and apply for project grants. Motivated to cover complex problems for advanced learners.   | Took up CT as a course though it’s new to him. Applied for JNTUH TEQIP III  |
| 4    | Ms.S. Yagna Sree           | Guided her to understand the significance of experiments during ICA & HDL Lab  | This helped her in the lab to explain the students clearly  |



Prof. OVPR Siva Kumar